

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideomi Suzawa et al.      Art Unit : 2812  
Serial No. : 09/852,672      Examiner : Alexander G. Ghyka  
Filed : May 11, 2001      Confirmation No.: 5083  
Notice of Allowance Date: July 27, 2006  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed July 27, 2006, enclosed is a completed issue fee transmittal form PTOL-85b. The fee in the amount of \$1400 for the issue fee is being paid concurrently herewith on the Electronic Filing System (EFS) by way of Deposit Account authorization. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: October 26, 2006

  
\_\_\_\_\_  
John F. Hayden  
Reg. No. 37,640

Customer No. 26171  
Fish & Richardson P.C.  
1425 K Street, N.W., 11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (202) 783-2331